

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : Jigang Liu
Application No. : 10/500,620
Filed : July 1, 2004
For : TRANSCEIVER WITH MULTI-STATE DIRECT DIGITAL
SYNTHESIZER DRIVEN PHASE LOCKED LOOP
Art Unit : 2618
Examiner : Nguyen, Tuan Hoang

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

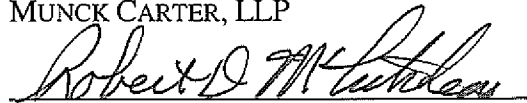
COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

In response to the Notice of Allowance dated March 23 2010, Applicants provides the following in response to the Statement of Reasons For Allowance:

Applicants believe that the claims and description of the invention are adequately described and set forth in the specification such that Applicants' claimed invention, and terms and features described therein, are readily understood by those of ordinary skill in the art as set forth in the allowed claims.

Respectfully submitted,

MUNCK CARTER, LLP



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Date: June 23, 2010

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